

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|---|---|------------------|---------|------------------|
| L1 | 20863 | memory and mode adj control | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/22 07:05 |
| L2 | 240 | 1 and instruction and accesses and based with address and command and command with external | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/22 07:06 |
| L3 | 46 | 2 and synchronization with clock and address adj buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/22 07:06 |
| L4 | 0 | 3 and delay adj control adj signal | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/22 07:07 |
| L5 | 0 | command adj unit adj outputs near1 command adj signal with activa adj state.clm. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/22 07:08 |